



**KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE, WARANGAL-15**  
(An Autonomous Institute under Kakatiya University, Warangal)

No.KITS/Acad/Autonomous-BoS (ECI)/2020

Date: 07.03.2020

**ORDERS**

Sub: KITS, Warangal - UGC Autonomous status - Constitution of Board of Studies (BoS) for B. Tech. Electronics Communication & Instrumentation Engineering (ECI) Programme.

Ref: 1. Letter No. F. 22-1/2-14(AC), Dt. 19.6.2014 from the Joint Secretary, UGC, New Delhi.

2. Letter No. 350/CDC/KU/2014, Dt. 18.8.2014. from the Registrar, KU, Warangal.

3. ECED office Note No. 77/ECE/KITS/2020, Date: 14.02.2020

With ref. to note 3 cited above, the Board of Studies (BoS) for B. Tech. Electronics Communication & Instrumentation Engineering (ECI) Programme has been constituted with the following members for a period of two (2) years with effect from the date of issue of these orders.

S. No.	Name of the Member	Designation	Position in BoS
1.	Dr. B. Rama Devi	Professor & HoD, ECED, KITSW	Chairperson, BoS
2.	Dr. K. Ashoka Reddy	Senior Professor of ECED, KITSW	Member
3.	Dr. G. Raghotham Reddy	Professor of ECED, KITSW	Member
4.	Dr. K. Sivani	Professor of EIED, KITSW	Member
5.	Dr. K. Venumadhav	Professor of EIED, KITSW	Member
6.	Dr. P. Chandra Sekhar	Professor & Head, Dept. of ECE, University College of Engineering, OU, Hyderabad	External Member (from renowned Academic Institute)
7.	Dr. T. Kishore Kumar	Professor, NIT, Warangal	External Member (from renowned Academic Institute)
8.	Dr. T. Srinivasulu	Professor, Dept. of ECE, KUICE, KU Dean, Faculty of Engineering, KU, Warangal	External Member (University Nominee)
9.	Dr. Y. Jagan Mohan Reddy	Senior Engineering Manager, Honewell Technology Solutions Labs Pvt Ltd, Hyderabad	External Member (from Industry)
10.	Sri R. Dileep	Sr. ASIC Design Engineer, Synopsis Inc, Pvt Ltd, Hyderabad	External Member (from Industry)
11.	Sri Bhaskar	Senior Staff Engineer Manager, Qualcomm	External Member (from Industry)
12.	Sri Gopi	Intel, Hyderabad	External Member (Post Graduate Meritorious Alumnus - Academia/Industry)
13.	Smt. M. Sreelatha	Professor of EIED, KITSW	Co-Opted Member-1
14.	Smt. S.P. Girija	Assoc. Prof. of ECED, KITSW	Co-Opted Member-2
	Sri E. Suresh	Assoc. Prof. of ECED, KITSW	Co-Opted Member-3

The Board shall exercise such powers, perform such duties and functions in accordance with the procedure laid down in UGC norms for the Kakatiya Institute of Technology & Science, Warangal.

The meeting of the Board shall ordinarily be convened as and when necessary, at least twice in a year.

However, any member of the Board of Studies shall cease to be a member when he vacates the office, which he was holding at the time of his/her appointment, unless otherwise permitted by the authority.

To  
The Persons concerned  
Copy to: 1. Secretary & Correspondent 2. Treasurer 3. All Deans  
4. All Heads of Departments 5. Academic Section

  
PRINCIPAL



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING  
KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE:: WARANGAL – 15  
(An Autonomous Institute under Kakatiya University, Warangal)

**MoM of M.Tech (Embedded System and VLSI) BoS- Dept. of ECE**

MINUTES OF THE MEETING

OF

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BOARD OF STUDIES (VIRTUAL MEETING)

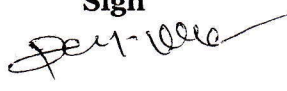
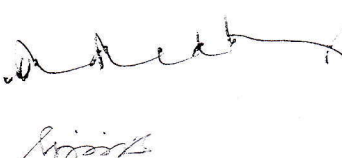


HELD AT 11:30 AM ON 17.10.2021 (Sunday)

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**Agenda:**

1. Approval of Scheme of M. Tech (ES & VLSI) I - IV Semesters
2. Approval of Syllabus of M. Tech (ES & VLSI) I - IV Semesters Courses.
3. Any other item with the permission of chair.

**Members Present:**

<b>S. No.</b>	<b>Name of the Member</b>	<b>Designation</b>	<b>Sign</b>
1.	Dr. B. Rama Devi	<i>HoD ECE, KITSW, Chairperson, BoS</i>	
2.	Dr. K. Ashoka Reddy	<i>Senior Professor of ECE Dept. KITSW, Member</i>	
3.	Dr. G. Raghotham Reddy	<i>Professor of ECE Dept. KITSW, Member</i>	
4.	S. P. Girija	<i>Assoc. Prof. of ECE Dept. KITSW, Member</i>	
5.	E. Suresh	<i>Assoc. Prof. of ECE Dept. KITSW, Member</i>	
6.	Sri Surya Kanth V Gangasetty	<i>Professor, School of Computing K L University, Vaddeswaram - 522502, Guntur District, Andhra Pradesh, External Member</i>	
7.	Dr.L.Anjaneyulu	<i>Professor &amp; Head, Dept. of ECE, NIT, Warangal, External Member</i>	
8.	Prof. T. Srinivasulu	<i>Professor in ECE Department of Electronics and Communication Engineering Kakatiya University College of Engineering and Technology, Kakatiya University, Warangal- 506009, India. Dean (Faculty of Engineering &amp; Technology), Kakatiya University., External Member</i>	
9.	Sri Ramesh Guptha Guntha	<i>Senior Staff Engineer Manager, Qualcomm, External Member</i>	
10.	Dr. Y. Jagan Mohan Reddy	<i>Senior Engineering Manager, Honeywell Technology Solutions Labs Pvt Ltd, Hyderabad, External Member</i>	
11.	Dr. G Sanath Kumar	<i>Deputy Director, Central Institute of Tool Design (CITD), Hyderabad, External</i>	

12. Sri V. Madhan Kumar *Member  
Member of technical staff, AMD  
India Pvt Ltd, Bangalore  
(M.Tech 2009-11), External  
Member*
13. A. Vijaya *Assoc. Prof. of ECE Dept. KITSW, Co-  
Opted Member-1*
14. Dr. M. Raju *Assoc. Prof. of ECE Dept. KITSW, Co-  
Opted Member-1*
15. Dr. V. Venkateshwar Reddy *Assoc. Prof. of ECE Dept. KITSW, Co-  
Opted Member-1*

*OK*  
*V. Venkateshwar Reddy*

**Resolutions:**

1. Virtual BoS meeting of Electronics and Communication Engineering Department was conducted on 17.10.2021, from 11:30 am to 2:30 pm through Google meet.
2. The above mentioned BoS members are present and offered their valuable suggestions.
3. BoS members of Electronics and Communication Engineering, KITSW approved
  - a. Scheme of M. Tech (ES & VLSI) I and IV Semesters
  - b. Syllabus of M. Tech (ES & VLSI) I and IV Semesters Courses & made following suggestions to incorporate.

S. No.	Course Code	Course Name	Suggestions by the External BoS members	Remarks
1.	P20EV101	Digital IC Design	<i>Unanimously approved</i>	
2.	P20EV102	Microcontroller based Embedded Systems	<b>T.Srinivasulu:</b> Modify the title Of Exceptions and Interrupts architecture with Interrupts architecture. Add 1 more text book. <b>Y. Jagan Mohan Reddy:</b> Use Job description of Embedded industry and modify the course content according to it.	
3.	P20EV103A	Semiconductor Device Modeling	<b>T.Srinivasulu:</b> Anything redundant to EDC course can be removed and suggested to identify the MOOC to learn the redundant topics of EDC course. Make course content more relevant to the Program  <b>Y. Jagan Mohan Reddy:</b> Anything redundant to EDC course can be removed.	

4.	P20EV103B	System Verilog for Design and Verification	<b>T.Srinivasulu:</b> Syllabus approved. Add one more text book.
5.	P20EV103C	Linux and Python programming	<b>G. Ramesh Gupta</b> Add Linux in memory management, interrupts handling using Linux, and schedulers in Linux environment topics. Focus on Linux OS concepts. <b>Y. Jagan Mohan Reddy:</b> Remove the redundant topics and add embedded Linux concepts
6.	P20EV103D	MOOC-I	<b>T.Srinivasulu:</b> Identify the list of suitable MOOC courses related to ES & VLSI.
7.	P20EV104A	Embedded System Design	<b>T.Srinivasulu:</b> Syllabus approved Add standard text book.
8.	P20EV104B	Wireless Technologies in Embedded systems	Syllabus approved.
9.	P20EV104C	Static Timing Analysis	Syllabus approved. Add one more text book.
10.	P20EV104D	MOOC-II	<b>T.Srinivasulu:</b> Identify the list of suitable MOOC courses related to ES & VLSI.
11.	P20EV105	Digital IC Design Laboratory	Syllabus approved. <b>T.Srinivasulu:</b> Mention the short description of the experiment( like tool, Set up required, objectives, outcomes and method of implementation of the experiment)
12.	P20EV106	Microcontroller based Embedded Systems Laboratory	Syllabus Unanimously approved, as presented <b>T.Srinivasulu:</b> Mention the short description of the experiment( like tool, objective and outcome of the experiment)
13.	P20EV201	Analog IC Design	<b>T.Srinivasulu:</b> Syllabus approved.
14.	P20EV202	Advanced System on chip design Internet of Things	<b>T.Srinivasulu:</b> Check the continuity of the course syllabus and modify the title accordingly.

15.	P20EV203A	Low Power VLSI Design	<b>V.Madhan Kumar:</b> Hands on based approach using Cadence or Synopsis to understand the concepts in better way. Add one more text book.
16.	P20EV203B	ASIC and SoC Design	Syllabus approved.
17.	P20EV203C	Model based embedded system design	Syllabus approved Add one more text book.
18.	P20EV203D	MOOC-III	<b>T.Srinivasulu:</b> Identify the list of suitable MOOC courses related to ES & VLSI.
19.	P20EV204A	Embedded Systems Design with RTOS	<b>T.Srinivasulu:</b> Identify the appropriate title of the course Add one more text book. <b>Y. Jagan Mohan Reddy:</b> Change the title of the course to RTOS
20.	P20EV204B	Multi core Architectures	Syllabus approved.
21.	P20EV204C	Radio Frequency IC design	Syllabus approved.
22.	P20EV203D	MOOC-IV	<b>T.Srinivasulu:</b> Identify the list of suitable MOOC courses related to ES & VLSI.
23.	P20EV205	Analog IC Design Laboratory	Syllabus approved. <b>T.Srinivasulu:</b> Mention the short description of the experiment( like tool, objective and outcome of the experiment)
24.	P20EV206	Advanced Embedded System design Laboratory	Syllabus approved. <b>T.Srinivasulu:</b> Mention the short description of the experiment( like tool, objective and outcome of the experiment)
25.	P20EV207	Mini Project with Seminar	<b>Surya Kanth V Gangashetty:</b> Allot internal guide right from internship and under same guide student has to continue the mini project as well as the dissertation
26.	P20EV301A	Embedded systems for industrial applications	<b>Y. Jagan Mohan Reddy:</b> Design the course with real time applications suitable to industry.

27.	P20EV301B	Internet of Things and Applications	<b>T.Srinivasulu:</b> Check the continuity of the course syllabus and modify the title accordingly.	
28.	P20EV301C	Artificial Intelligence and Machine Learning	Syllabus approved.	
29.	P20EV303	Dissertation <i>Phase-I</i> / Industrial Project (to be continued in IV - semester also)	Syllabus approved.	
30.	P20EV304	Internship Evaluation	Syllabus approved.	
31.	P20EV401	Dissertation <i>Phase-II</i>	Syllabus approved.	

Note:- Identify the road map for the core placements of the M.Tech-Embedded System and VLSI Course.

Finally, Professor Dr.B.Ramadevi, Chairperson BoS, HoD dept. of ECE, thanked all the BoS members, Principal, AAC members and faculty of Electronics and Communication Engineering Department for giving their suggestions while preparing the scheme and drafting the syllabus.

Note:

- i). I request the CCPC conveners to identify the road map for the core placements of the M.Tech-Embedded System and VLSI Course.
- ii). I request concerned CCPC convener to incorporate the suggestions/inputs (Listed in the above table) given by the BoS Members. Please send the modified syllabus along with cam table, KSQ and worksheet to vr.ece@kitsw.ac.in.